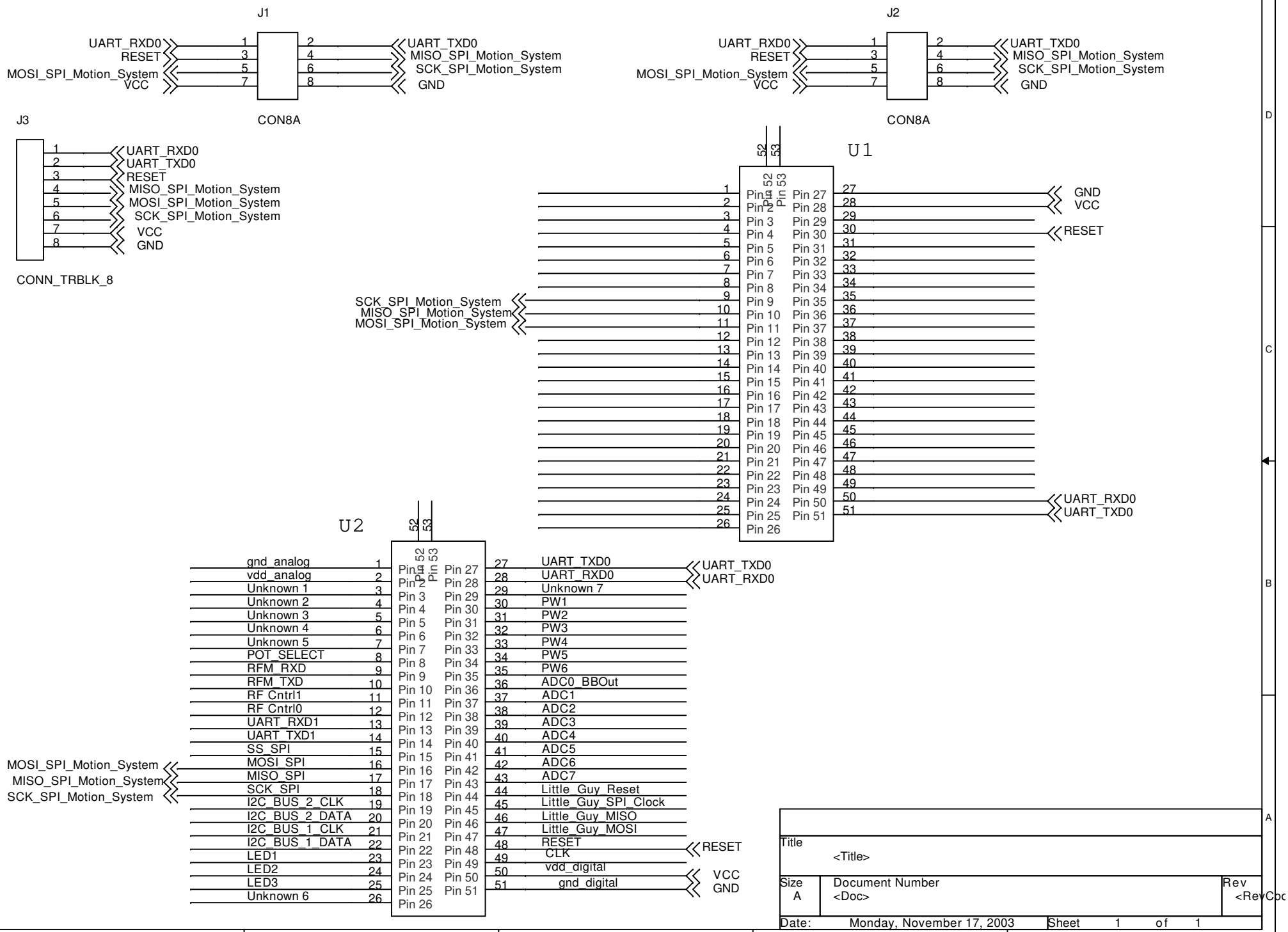
J1
UART_RXD0
RESET
MOSI_SPI_Motion_System
VCC
UART_TXD0
MISO_SPI_Motion_System
SCK_SPI_Motion_System
GND
CON8A
J2
UART_RXD0
RESET
MOSI_SPI_Motion_System
VCC
UART_TXD0
MISO_SPI_Motion_System
SCK_SPI_Motion_System
GND
CON8A
J3
UART_RXD0
UART_TXD0
RESET
MISO_SPI_Motion_System
MOSI_SPI_Motion_System
SCK_SPI_Motion_System
VCC
GND
CONN_TRBLK_8
U1
Pin 52
Pin 53
GND
VCC
RESET
SCK_SPI_Motion_System
MISO_SPI_Motion_System
MOSI_SPI_Motion_System
UART_RXD0
UART_TXD0
U2
gnd_analog
vdd_analog
Unknown 1
Unknown 2
Unknown 3
Unknown 4
Unknown 5
POT_SELECT
RFM_RXD
RFM_TXD
RF Cntrl1
RF Cntrl0
UART_RXD1
UART_TXD1
SS_SPI
MOSI_SPI
MISO_SPI
SCK_SPI
I2C_BUS_2_CLK
I2C_BUS_2_DATA
I2C_BUS_1_CLK
I2C_BUS_1_DATA
LED1
LED2
LED3
Unknown 6
MOSI_SPI_Motion_System
MISO_SPI_Motion_System
SCK_SPI_Motion_System
UART_TXD0
UART_RXD0
Unknown 7
PW1
PW2
PW3
PW4
PW5
PW6
ADC0_BBOut
ADC1
ADC2
ADC3
ADC4
ADC5
ADC6
ADC7
Little_Guy_Reset
Little_Guy_SPI_Clock
Little_Guy_MISO
Little_Guy_MOSI
RESET
CLK
vdd_digital
gnd_digital
RESET
VCC
GND
UART_TXD0
UART_RXD0
Title
<Title>
Size
A
Document Number
<Doc>
Rev
<RevCod
Date:
Monday, November 17, 2003
Sheet
1
of
1